

FIG. 1
PRIOR ART

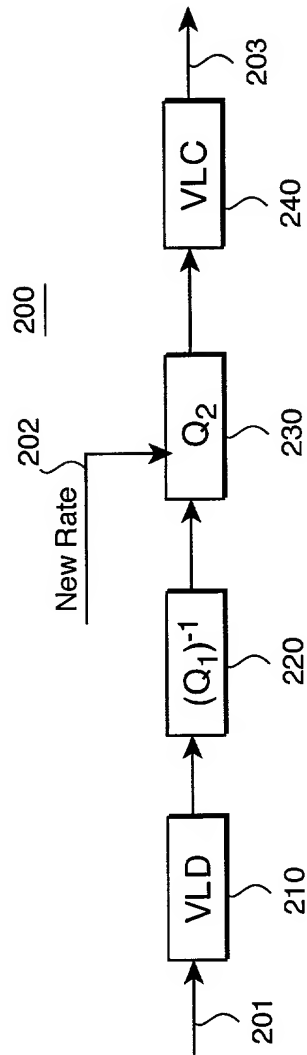


FIG. 2
PRIOR ART

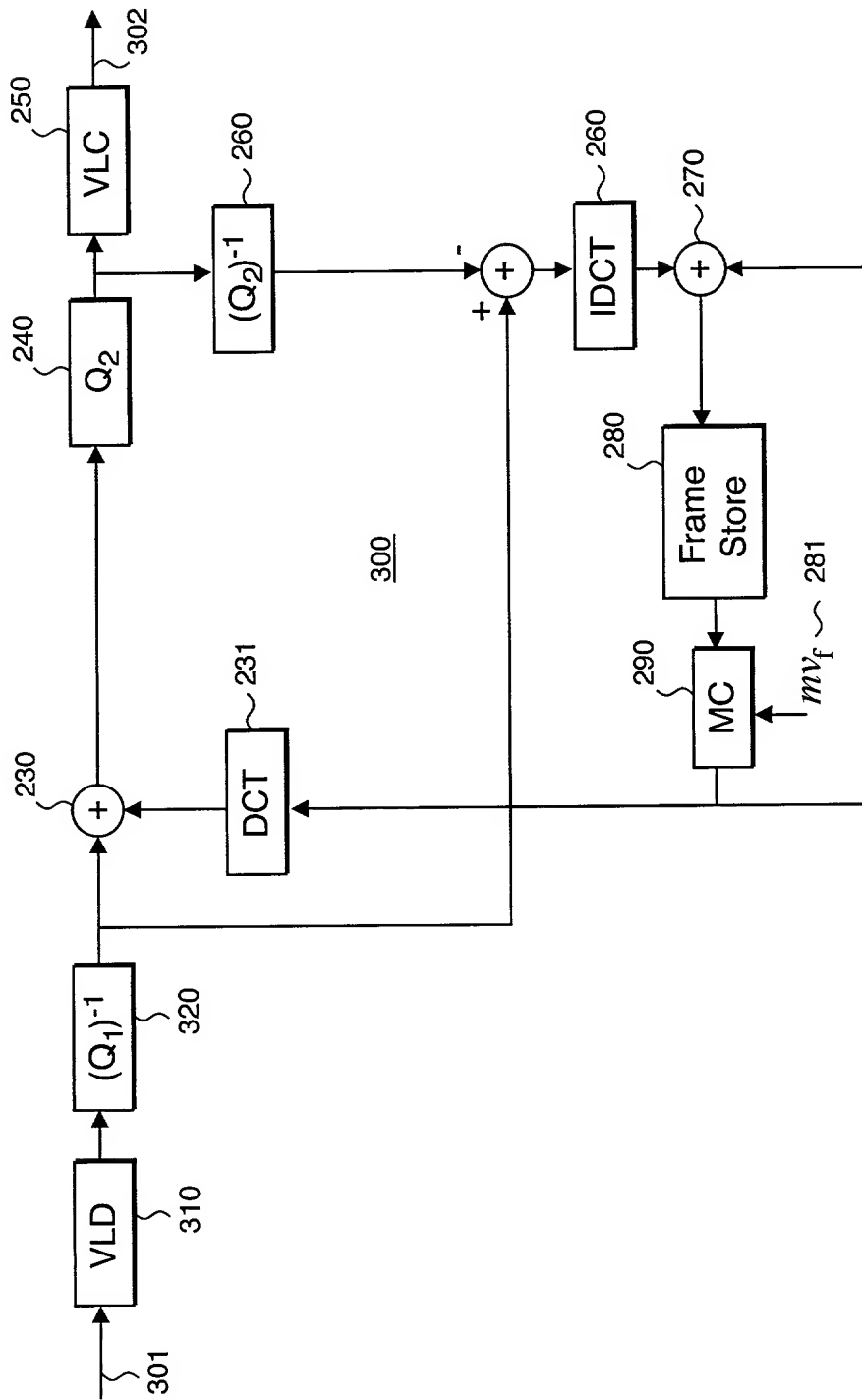


FIG 3
PRIOR ART

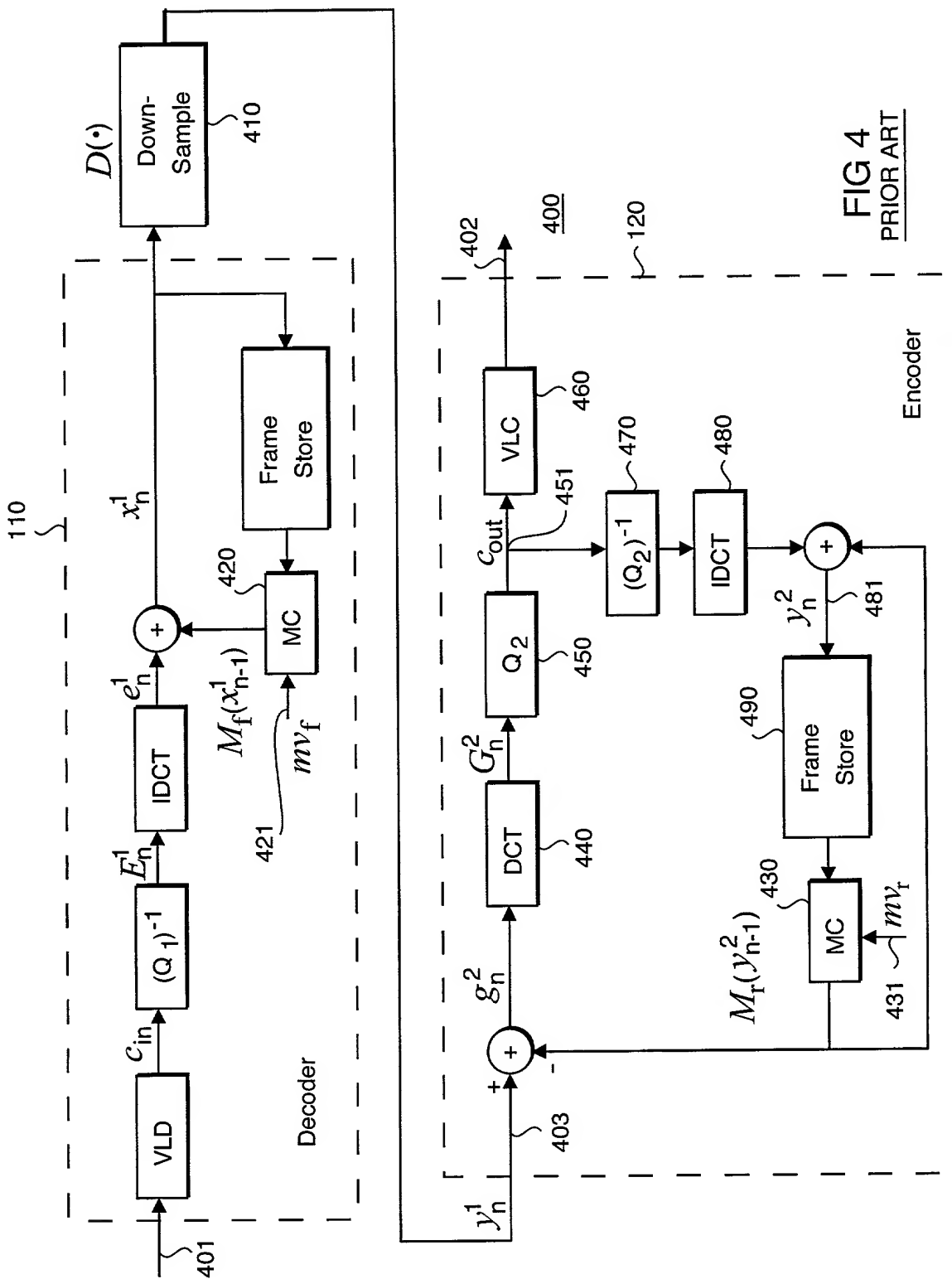


FIG 4
PRIOR ART

FIG. 5

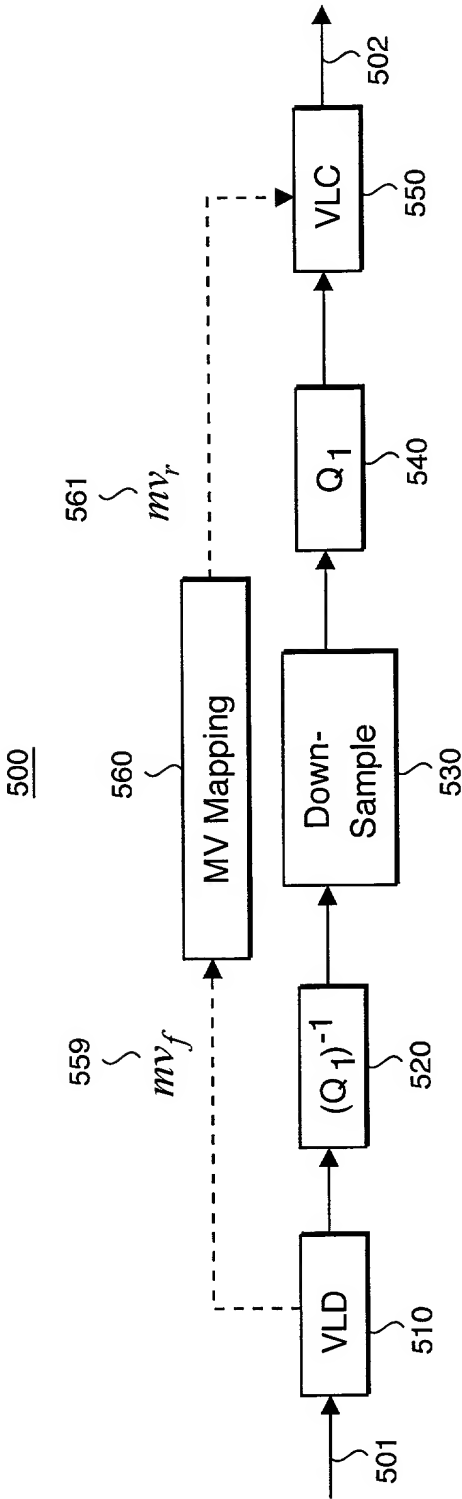


FIG 5
PRIOR ART

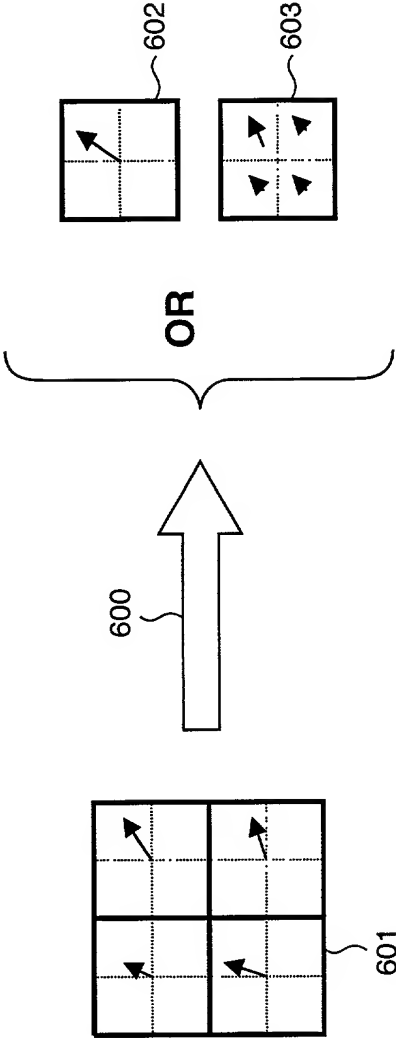
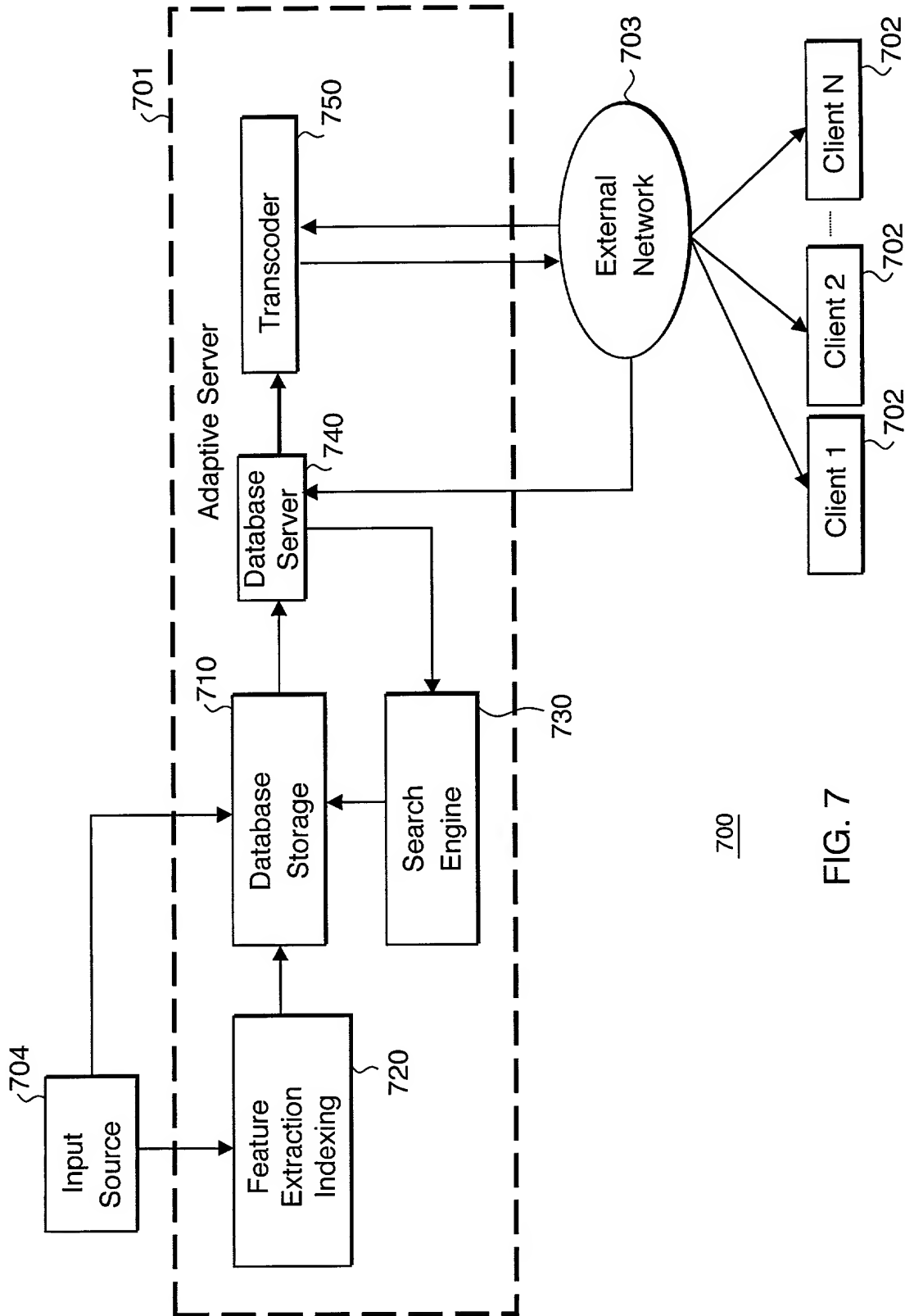


FIG. 6
PRIOR ART



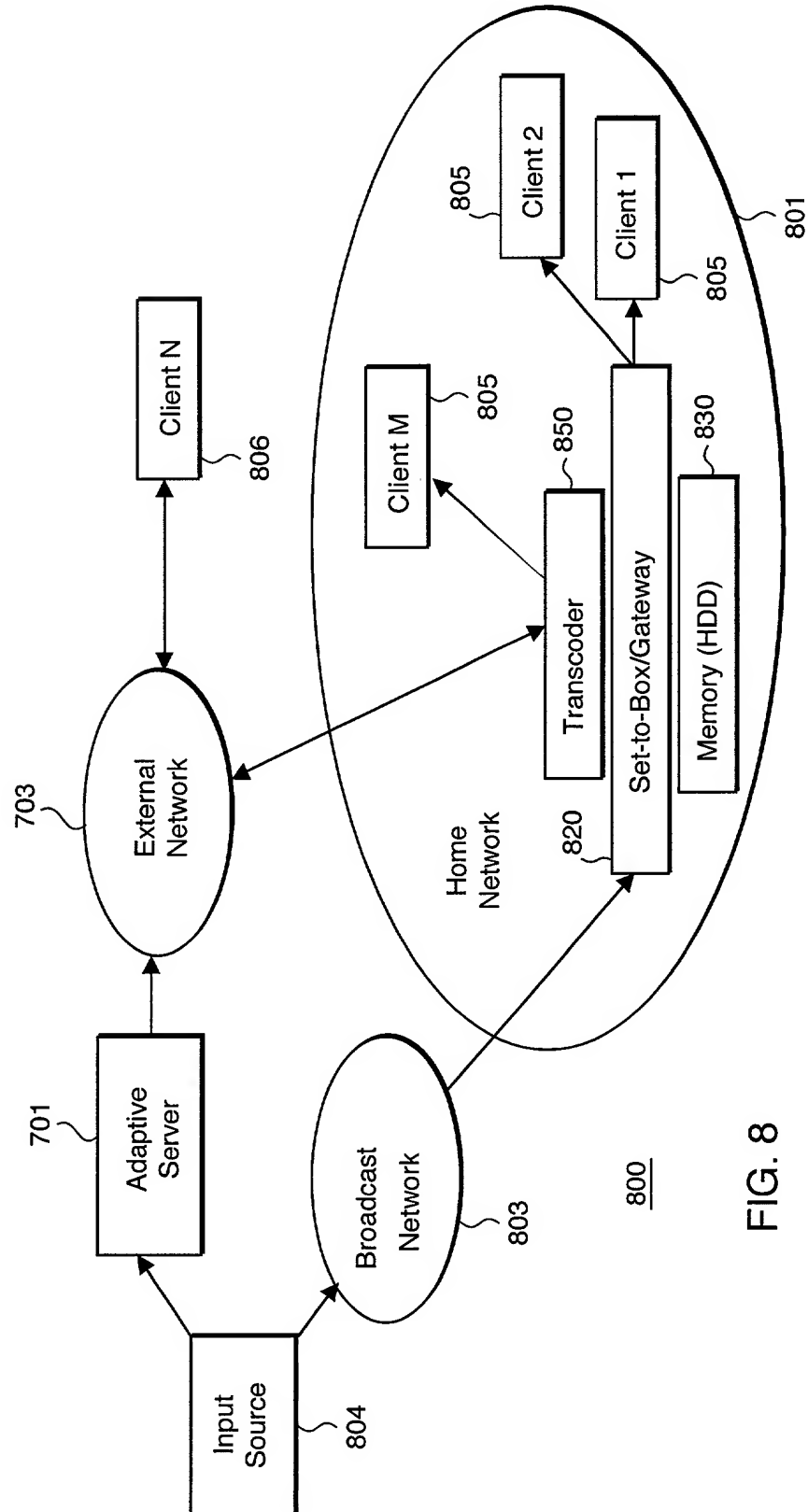


FIG. 8

FIG. 8 is a block diagram of a video transcoder system.

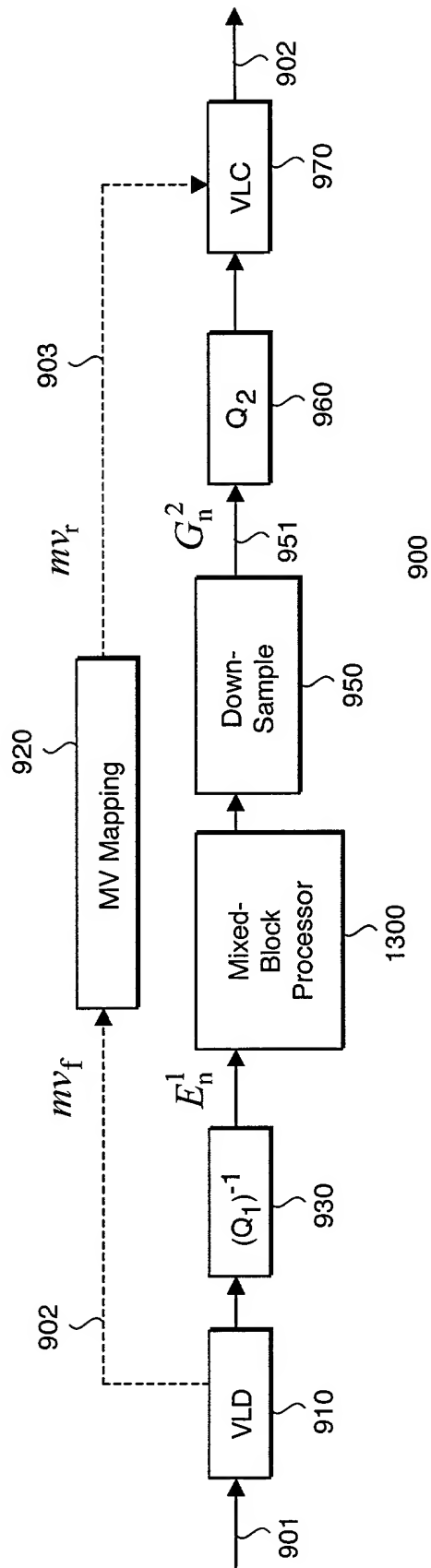


FIG. 9

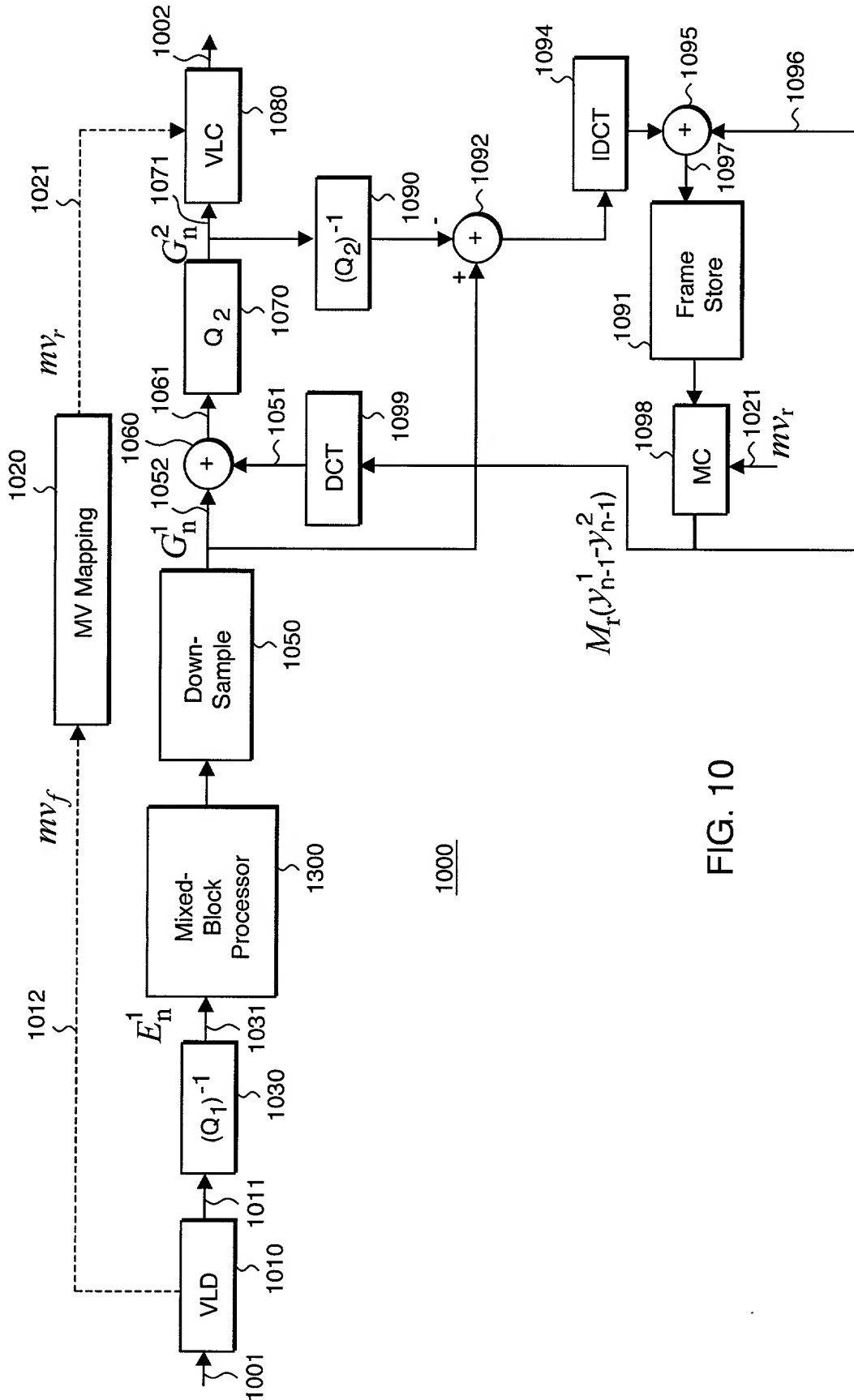
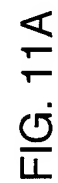


FIG. 10



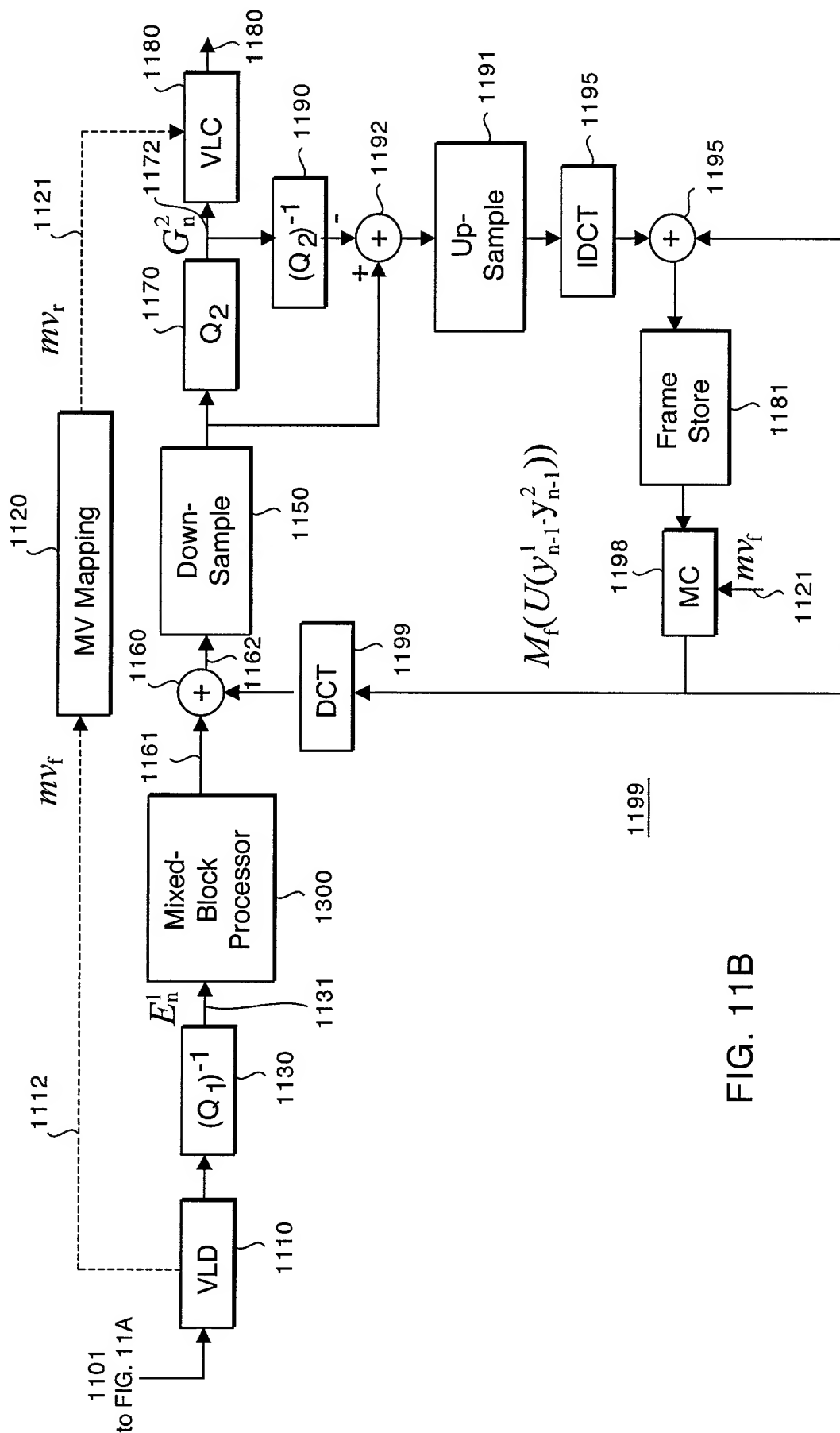


FIG. 11B

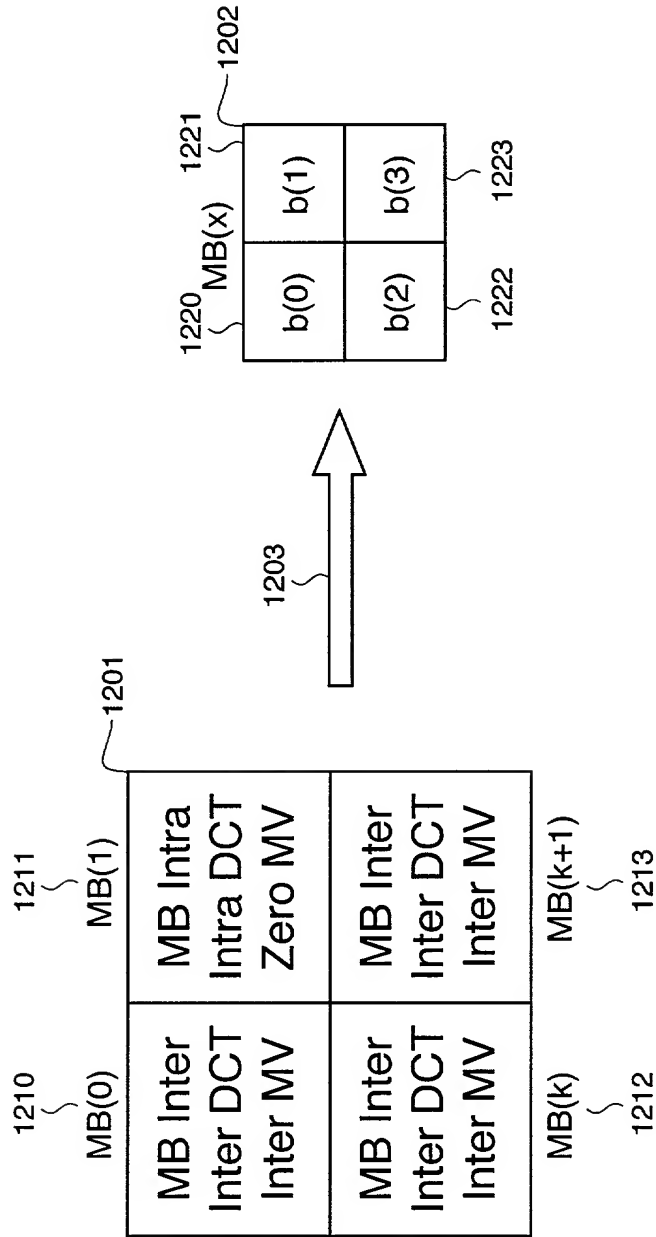


FIG. 12



FIG. 13

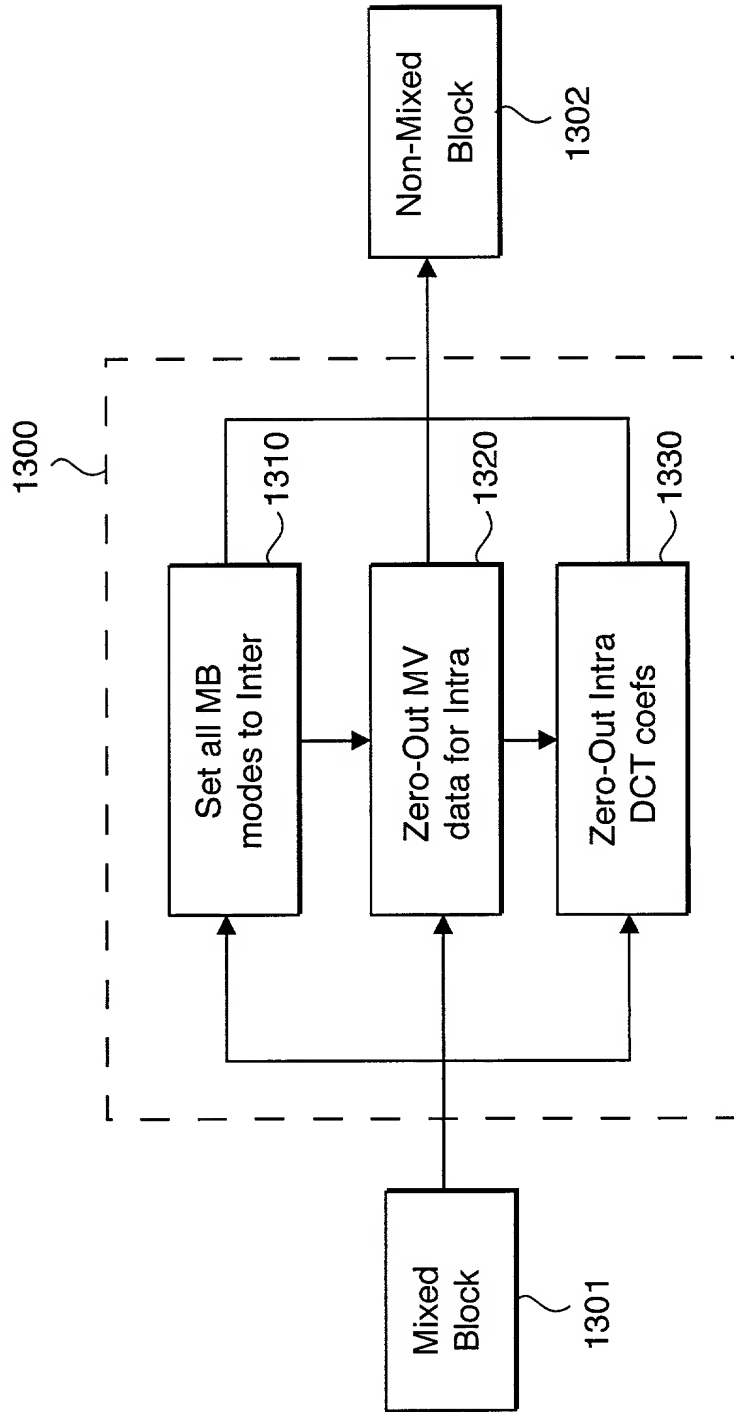


FIG. 14A

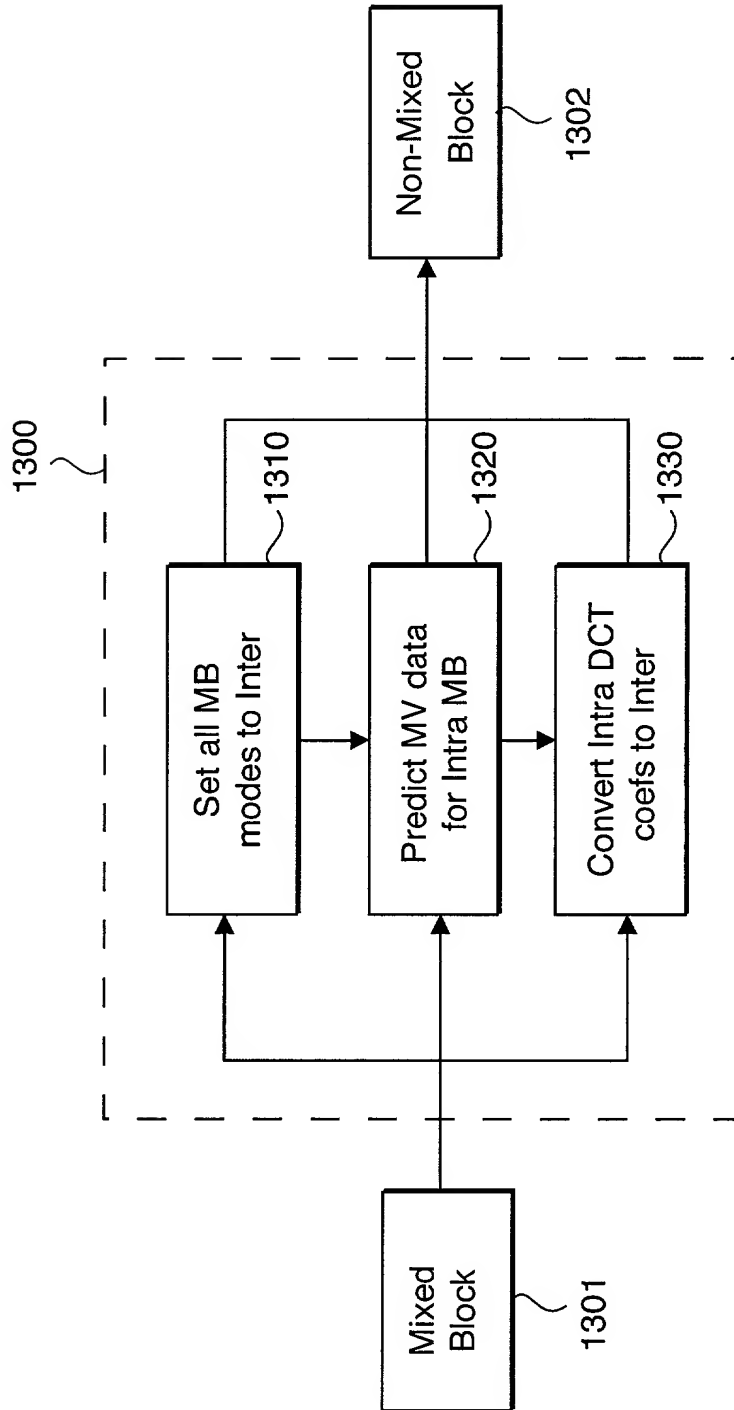


FIG. 14B

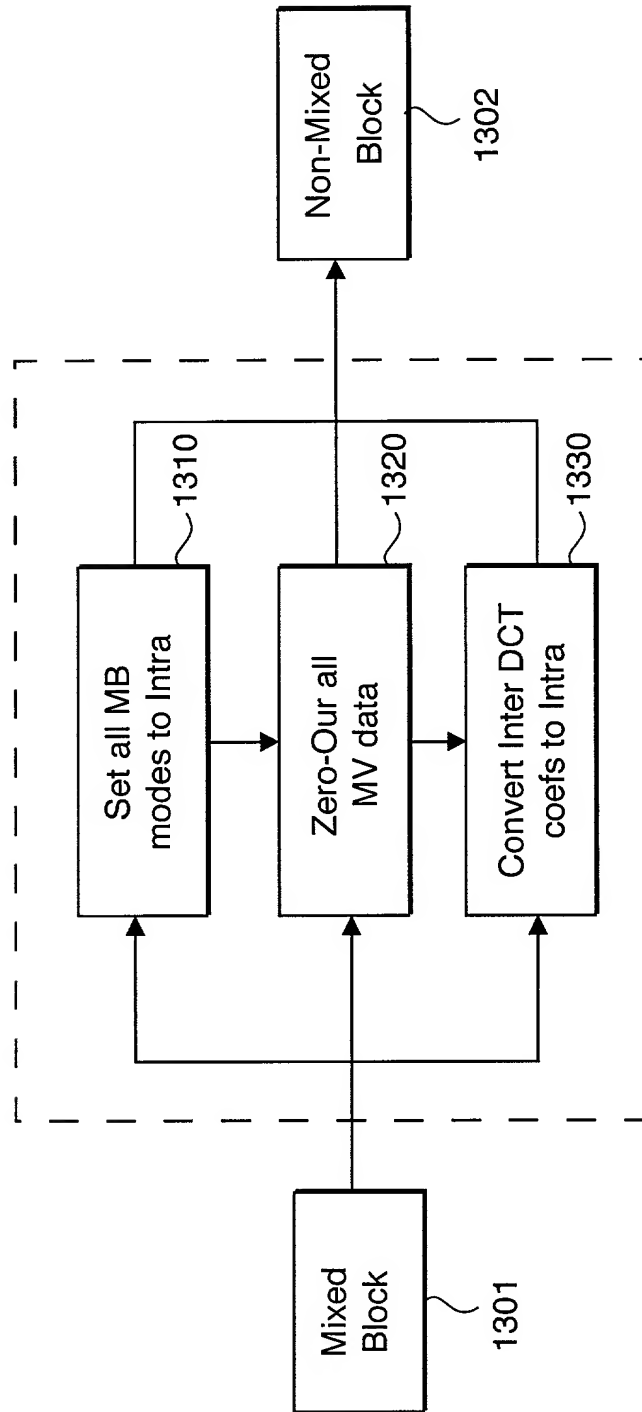


FIG. 14C

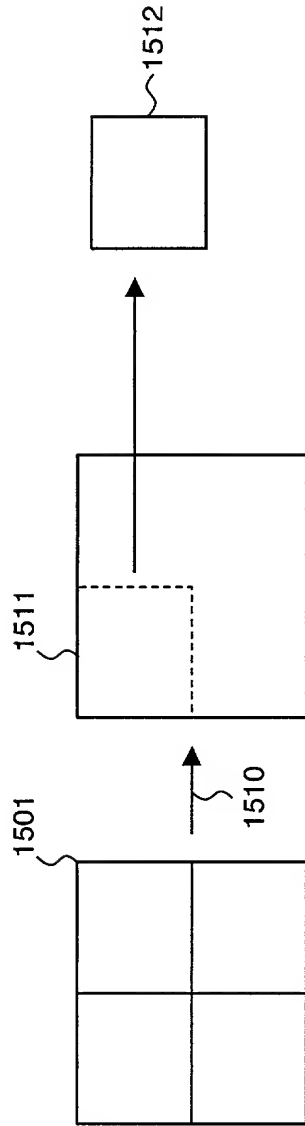


FIG. 15A
PRIOR ART

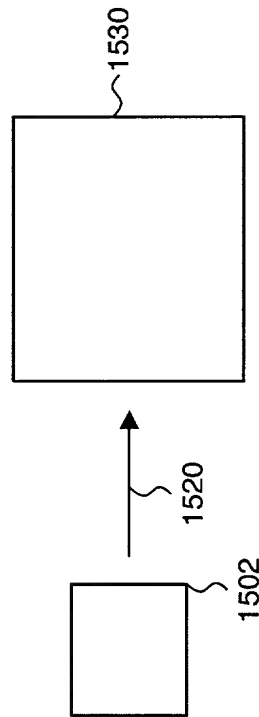


FIG. 15B

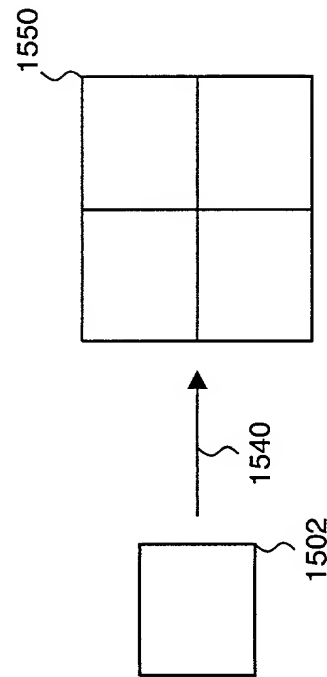


FIG. 15C

FIG. 15

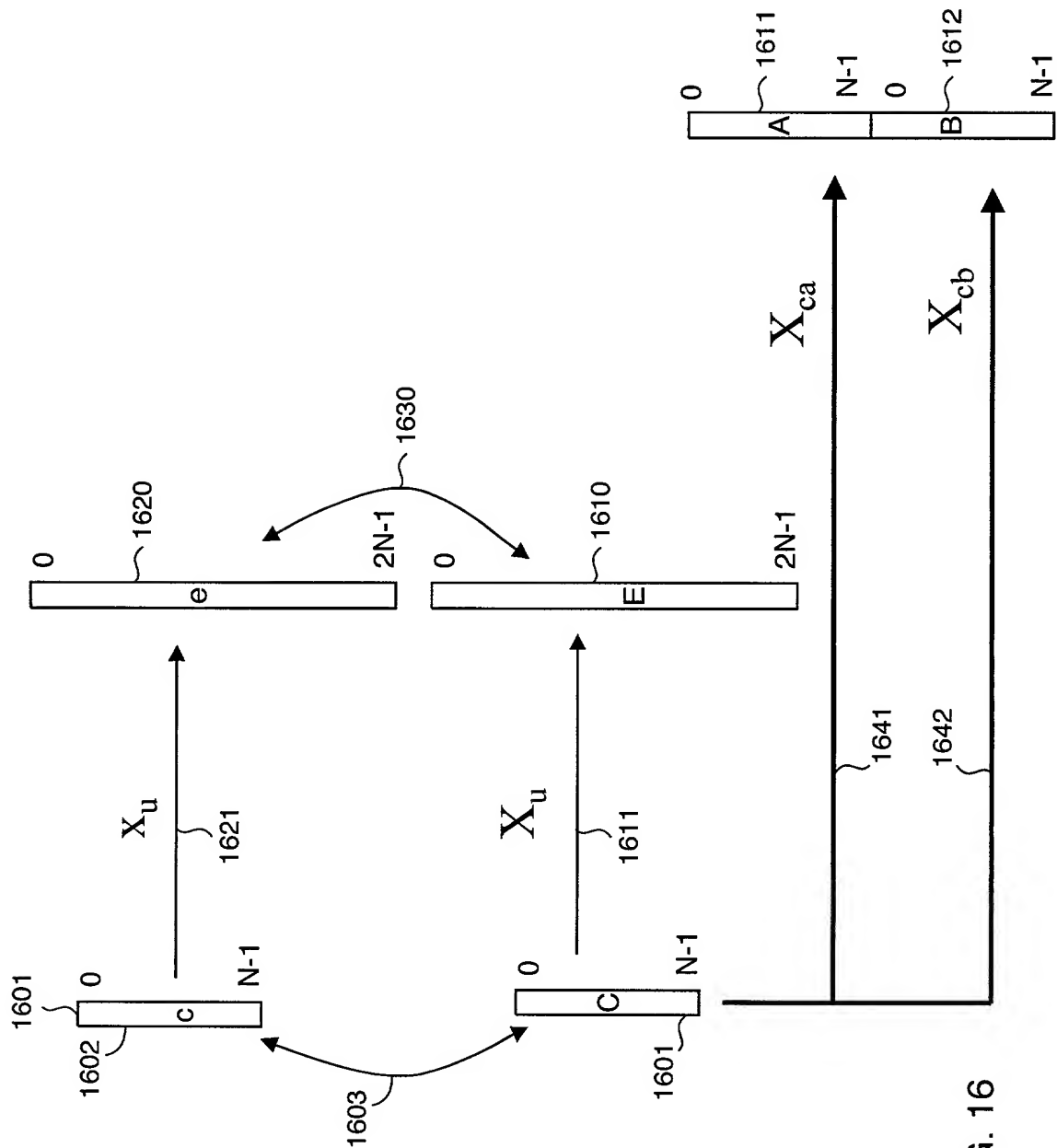


FIG. 16

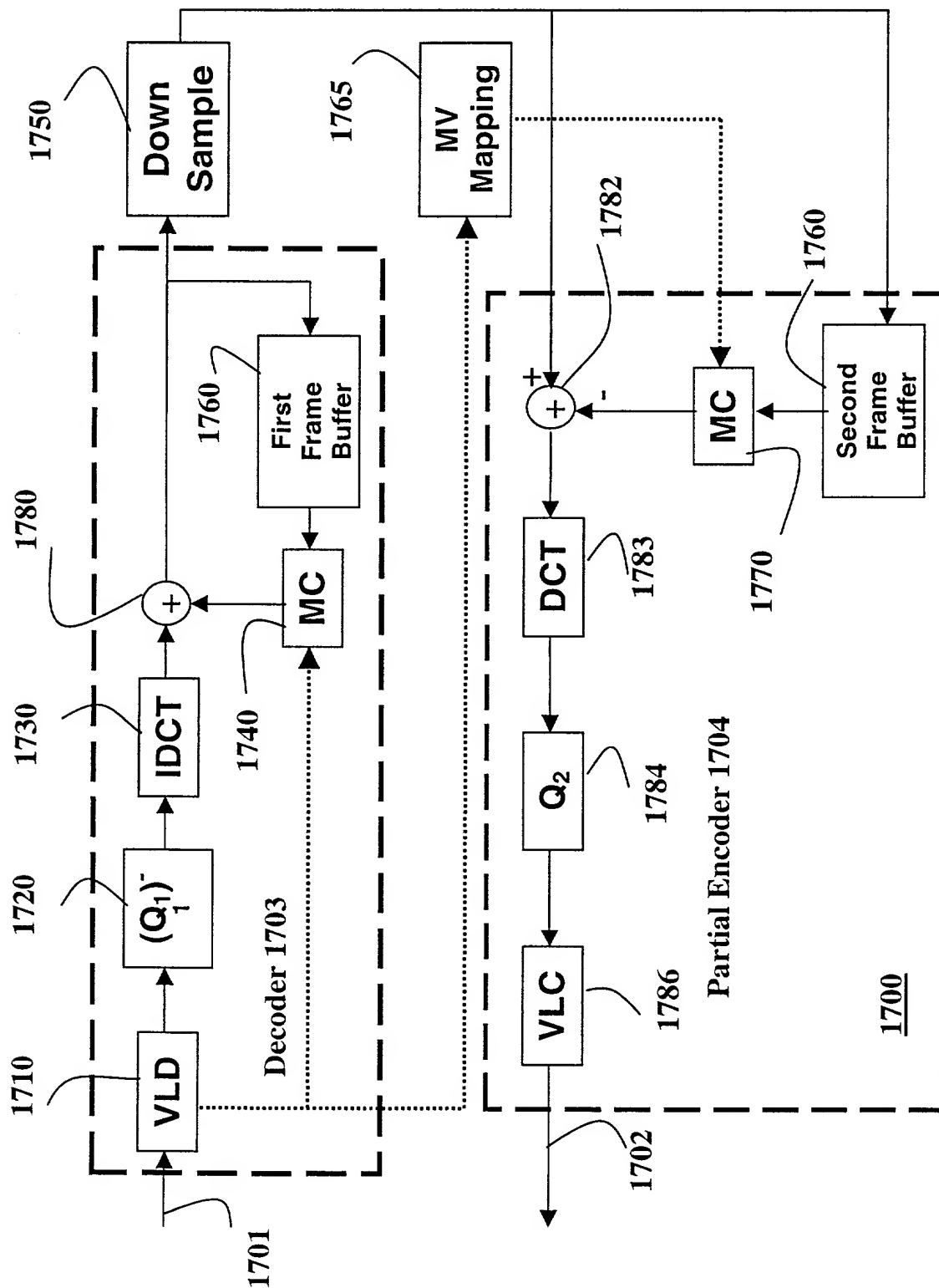


Fig. 17